



SOCIETY FOR ELECTRONIC TRANSACTIONS AND SECURITY [SETS]

CIT Campus, MGR Knowledge City, Taramani,
Chennai – 600 113, India.

Adv. No: SETS/Chn/Rec/Proj/2020/02

Date: 29/Jan/2020

Society for Electronic Transactions and Security [SETS] is a non-profit society dedicated to research and development in the field of Information Security under the initiative of the Office of the Principal Scientific Advisor (PSA) to Government of India.

Applications are invited for filling up of **Three Project Associates** for technical needs of advanced cyber security research and development from the eligible candidates who fulfill the requisite qualifications/ experience mentioned below. The positions as proposed are purely temporary and would be filled on Contract basis with consolidated salary under project mode. The PAs will be engaged initially for a period of one year and the contract may be renewed for a period of one more year, based on the assessment of their contributions to the project. There is no scope of continuation / regularization / absorption under any circumstances.

The descriptions of positions, detailed qualification requirements and salary are given below:

Name of the Post	PROJECT ASSOCIATE – HARDWARE SECURITY
Number of Posts	THREE
Age Limit	Not more than 35 years as on 01.02.2020
Essential Qualification	<ul style="list-style-type: none">i. First Class B.E / B.Tech ECE/EEE/E&I with valid gate score.orii. First Class M.Tech /M.E (Applied Electronics/VLSI Design/Embedded System/ Communication System)
Work Experience / Desirable criteria	<ul style="list-style-type: none">• Candidates with experience in Cryptology/ FPGA development.• Candidates with valid GATE Score
Areas of Skill sets/ Knowledge required	<ul style="list-style-type: none">a) Knowledge of Cryptography.b) Knowledge of FPGA implementation flow and Xilinx Vivado tools.c) Hands-on experience in simulation using MATLAB/ModelSim and FPGA realization using VHDL/Verilog programming.
Remuneration	Consolidated salary would be in the range of Rs.30,000 to Rs. 40,000 per month commensurate with qualification, skill sets and relevant experience of candidate. (based on performance of the first year 5-8% increment may be considered)

Equivalent work experience in lieu of higher degree for project associate may be considered as follows:

- Engineering Post-Graduates : Two years
- Engineering Doctorates: Four Years

Application Procedure:

- 1. Only applications received via email will be considered.** The Subject line in the email should contain the “**Application for the post of Project Associate – Hardware Security**”. The candidate is required to attach Personal Particulars Form (pdf format) duly filled and signed. The Personal Particulars form is hosted at SETS homepage <https://setsindia.in/>.
- 2. The email should be sent to hrrca2020@setsindia.net**
- 3. The last date for receiving applications by Email is 19.02.2020.**
- 4. Shortlisted candidates would be required to attend a Written Test and/or Interview at SETS, Chennai. The date and time for this would be intimated to shortlisted candidates by Email.**

Terms and Conditions:

1. The shortlisted candidates would be required to bring all their original testimonials for verification on the test/interview day.
2. No TA/DA will be given to candidates appearing for written test or interview.
3. The prescribed qualifications are minimum and mere possession of the same does not entitle the candidate to be called for the written test or interview. The decision of the Executive Director of SETS in all matters relating to eligibility, acceptance or rejection of the applications, cancellation of advertisement and filling up or not filling up of post will be final and no inquiry or correspondence will be entertained in this matter.

Executive Director