Webinar on

"Physically Unclonable Functions (PUFs) Design and Applications"

Speakers Profile

Prof. Udayan Ganguly

Udayan Ganguly is working as a professor in IIT Bombay at Electrical Engineering department. He has received his B.Tech. degree in Metallurgical Engineering from the IIT Madras, in 2000 and M.S. and Ph.D. degrees in Materials Science and Engineering at Cornell University, Ithaca, NY, in 2005 and 2006 respectively. In 2006, Udayan joined Applied Materials to serve as the technical lead for Flash Memory Applications Development at Applied Materials' Front-End Product Division, Sunnyvale, CA. He has joined Dept. of Electrical Engineering, IIT Bombay in 2010 and is currently working as Professor at IIT Bombay. He has authored/ co-authored 50+ journal, 80+ conference and 25+ patents (applied/granted). His research interests are in semiconductor device physics and processing technologies for advanced memory, computing, and neuromorphic systems. He has contributed to the TIFAC National Vision for ICT 2035. He works to augment national semiconductor manufacturing capability at Semi-Conductor Labs, Chandigarh, for which he has won the Dr. PK Patwardhan Technology Development Award 2018

Dr. N. Nalla Anandakumar

Nalla Anandakumar is working as Scientist at Society for Electronics Transactions and Security [SETS], India. In his 12 years of service at SETS, he has served as PI, Co-PI and team member of various funded R&D projects in the field of hardware security. He was a visiting researcher at Nanyang Technological University (NTU), Singapore. He received his PhD degree from IIIT-Delhi, India in 2020, M.E. degree from Anna University, India in 2006 and B.E. degree from Government College of Engineering, Salem, India in 2003. He has authored more than 15 papers in refereed international conferences and journals such as the IEEE Transactions on Circuits and Systems and the ACM Transactions on Reconfigurable Technology and Systems. He also served as reviewer for few top journals such ACM/IEEE transactions. His research interests include hardware security and trust, such as physically unclonable functions, efficient and secure implementations of cryptographic primitives and side-channel attacks.