Speakers' Profile

Prof V Kamakoti **IIT Madras**	Dr V Kamakoti is working as a professor in IIT Madras at computer science and engineering (CSE) department. He has obtained both his Masters by research degree and doctoral degree from IIT madras. His areas of specialisation include software for VLSI, reconfigurable systems design and computer architecture. Prof V Kamakoti has guided several undergraduate, postgraduate and doctoral students and has published in various national and international journals. He has won several awards for his contributions in his research field. Most recently, he has received DRDO academic excellence award in recognition of his contribution to various programs of DRDO.
Dr P V Ananda Mohan **C-DAC Bangalore**	Dr P.V. Ananda Mohan obtained his Ph.D degree in Electrical Communication Engineering from Indian Institute of Science, Bangalore. His research interests are in the area of Analog VLSI design, VLSI architectures, Cryptography. He has published in these areas in refereed international journals and conferences. His book Switched Capacitor Filters: Theory, Analysis and Design coauthored with Dr. M. N. S. Swamy and Dr. V. Ramachandran was published by Prentice-Hall (London). He has authored two more books Residue Number Systems: Algorithms and Architectures published by Kluwer Academic Publishers and Current-mode VLSI Analog Filters: Design and Applications by Birkhauser. He is a Fellow of IEEE & IETE. He has received the Ram Lal Wadhwa Gold Medal Award from the Institution of Electronics and Communication Engineers (India) and Indira Priyadarshini Award.
Prof C E Veni Madhavan **IISc**	Dr C E Veni Madhavan is a faculty at Indian Institute of Science Bangalore in Computer Science and Automation department. He has obtained his PhD (Control theory) from IISc Bangalore. Prior to joining IISc as a faculty, he worked for Air India and National Informatics Centre. His research interests include cryptanalysis of large-scale integer factoring and statistical analysis and cognitive network modeling of verbal and visual fields. He has led several scientific, industrial consulting and development projects sponsored by government and corporate sectors in these areas and has guided over 100 students. Having more than 60 research publications to his credit, he has also co-authored a recently published book on Public-key Cryptography. In 2001, Mathematical Association of India awarded him for his distinguished services in mathematics education and research.

Mr V Anil Kumar **CSIR 4PI**	Mr V Anil Kumar is a Principal Scientist at CSIR Fourth Paradigm Institute, Bangalore. His research interests are in Cyber Security, High Performance Computing and Protocol Engineering. He has more than 30 research publications and has filed International patents in US and Europe in the areas of Cyber Security. He has worked on a large European Union Project called OneLab2 to establish a geographically distributed and federated network testbed among PlanetLab US, PlanetLab Europe and PlanetLab Japan. He was the Chief Investigator of multiple cyber security research projects from Ministry of Information Technology and CSIR. He has also initiated a long-term programme on Cyber Security Research and Observation (CySeRO) under 12 Five Year Plan.
Mr Jose Varghese **Paladion Networks**	Mr Jose Varghese is a Paladion co-founder. He currently heads technical delivery of Paladion's CyberActive Services. He has over 20 years of IT services delivery experience, out of which 15 years is with Paladion and is focused on cybersecurity. At Paladion, he started with security product deployment projects focusing on Firewalls, Intrusion Prevention Systems and Anti Virus. Jose was also the lead instructor for Paladion's security workshop series titled "Information warfare". He was also the lead instructor for SANS training programs for GSEC and GCIH courses which were held in India. He also manages the Application Security Testing team which focuses on delivering Vulnerability Management programs for enterprise customers. Jose received his B.Tech in Applied Electronics in 1995 and worked with Wipro for 5 years before he co-founded Paladion in 2000.
Dr Lakshmi Devi **SETS**	Dr Lakshmi Devi is working as Scientist C at SETS and has obtained her PhD (Information Security) from QUT, Australia. Her PhD work is on the mathematical modeling of the denial-of-service resilient key exchange protocols. Her current research interest is in the area of security protocols for key mangement and authentication. Her recent focus is on the analysis of Blockchain platform, namely multi-chain and using game theoretic approaches for cyber security. She also worked in other areas such as design and development of DoS countermeasure and secure delegation of cryptographic computations. She has more than 10 publications in reputed International conferences (RSA, ISC & Indocrypt) to her credit.

Mr Vijayakumar Athithan **SETS**	Mr Vijayakumar Athithan has around 18 years experience in software field and is leading consulting assignments in startups and development of indigenous security solutions at SETS. Prior to that, he worked for HCL and Infosys for a decade. He has proven strong program, project & process management experience in corporate environment and executed annual excellence award winning project (best project among more than 1500 projects) in Infosys. Played Delivery unit anchor role for Retail–CPG Chennai Infosys practice and involved in CMM Assessment(HCL) and ISO 27001 compliance(Infosys) initiatives. Certified in SCJP, SCBCD, PMP, CEH, CloudF and ITIL. He did his Master's degree in computer science from M.K.University, Madurai and obtained management education from RMIT University Melbourne.
Dr Sumanta Sarkar **TCS, Hyderabad**	Dr Sumanta Sarkar is a Scientist in TCS, Hyderabad and also an Adjunct Faculty of CR Rao AIMSCS. He did his masters in Mathematics at University of North Bengal and PhD from Indian Statistical Institute, Kolkata under the supervision of Prof. Subhamoy Maitra. Sumanta has completed his Post Doctoral Research from INRIA Paris - Rocquencourt, France and University of Calgary.
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Dr Chester Rebeiro **IIT Madras**	Dr Chester Rebeiro is an Assistant Professor at IIT Madras. He completed his PhD from IIT Kharagpur and a post-doc from Columbia University. His research interests are in hardware security, applied cryptography, side channel analysis, and operating system security. He has one patent filed for his innovation on obatining resistance to Cache Timing Attacks for Block Cipher Encryption. He has coauthored a book titled Timing Channels in Cryptography: A Micro-Architectural Perspective published by Springer. He sis currently working on formal verfication approaches to hardware security and fault attacks.

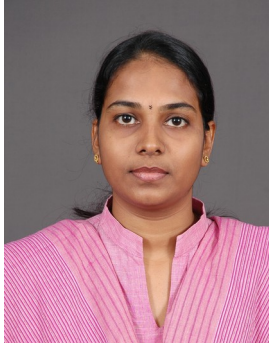|	
Mr Dinesh Thirumurthy **HAKR Technologies**	Mr Dinesh Thirumurthy did his MS in CS from Indian Institute of Science and BTech in CS from IIT Madras. He brings to the table a keen sense of product development and ownership. He worked in systems development in the areas of distributed systems, big data, networking, caching, bandwidth management, filesystems and embedded engineering over the last 2 decades, over at my current company HAKR Technologies, previous company Radien Software, and also at NetApp, amazon.com and ERNET. He enjoys a full product development cycle and driving the product into the market. He brings in hands on programming, technical leadership, management expertise and top notch product focus. He is an awarded engineer from NetApp. But being another common man in India, he just wishes to serve the nation through his software expertise.
Mr Nalla Anandakumar **SETS**	Mr Nalla Anandakumar is working as Scientist at SETS. In his 10 years, he was part of various R&D projects in the field of hardware security. Earlier he worked as Lecturer at Jaya Engineering college, Chennai. He was also a visiting researcher at Nanyang Technological University (NTU), Singapore. He received his M.E. degree from Anna University, Chennai. Currently, he is pursuing Ph.D. degree at IIIT-Delhi, India. He has co-authored 10 publications at recognized transactions and conferences, and was a program chair of ASK-2014. His research interests include physically unclonable functions (PUFs), efficient and secure implementations of cryptographic primitives, side-channel attacks and hardware trojan detection.
Dr Prem Laxman Das **SETS**	Dr M. Prem Laxman Das has completed Ph.D. in Maths from Indian Statistical Institute. He works broadly in the domain of algorithmic aspects of algebra and number theory. In cryptology, his interests include cryptanalysis of public key systems, pairing-based crypto with applications to cloud computing security and aspects of post-quantum cryptography. He is currently executing a project on cryptography for MANETs funded by BEL.
Mrs Lakshmi Eswari **C-DAC Hyderabad**	Mrs P R Lakshmi Eswari is currently working as Joint Director (e-Security) at C-DAC Hyderabad. She did her Bachelors' and Masters' degree in Computer Science and Engineering. She has has 15 years of experiecne in Research and Teaching at C-DAC and NIT Warangal. Her research interests are in End-point security, device control, anti-malware solutions and mobile security. She is currently executing a project on Blockchain technology. She is a member of Advisory Board of SETS.

Ms A Suganya **SETS**	Ms Suganya Annadurai is working as Scientist D at SETS and obtained her MTech in VLSI Design from SASTRA University, Thanjavur. In her 14 years of experience at SETS she has developed core competency in side channel analysis and secure and efficient realisation of cryptography modules. Her research interests include side channel analysis attacks and countermeasures, analysis and design of efficient and secure crypto modules for communication systems, ubiquitous devices, and embedded devices. She played a major role in the establishment of Side channel analysis evaluation laboratory at SETS for the evaluation of crypto modules. She has good number of publications in reputed International conferences to her credit.
Mr K Thirupathi **SETS**	Mr K Thirupathi did his M.Tech in Cyber Security from Amrita University, Coimbatore . He is working as Senior Network Analyst at SETS. He has more than five years of experience in developing network security products and providing cyber security solutions to the end user. His current research focus is in the area of Cyber Security, with emphasis on Internet Traffic Analysis, Machine Learning and the detection of network attacks. He is currently executing a project on Deep Packet Analyis funded by BEL.
Mr S Karthikeyan **SETS**	Mr Karthikeyan did his M.Tech in Information Security and Computer Forensics at SRM University, Chennai. His areas of work include network security, cloud computing, digital forensics, intrusion detection and prevention analysis. He also played a role of a cyber forensic consultant for the state police departments and other governmental organizations to aid them on security policies and controls. He is a TigerScheme Associate PenTester, an EC-Council Certified Security Analyst and a RedHat Certified Engineer. Currently he is working as a Project Associate and Network Administrator at SETS.