

# National Workshop on “Cyber Security for Critical Infrastructure”

Panel Discussion (3.00 PM to 4.30 PM) on 28<sup>th</sup> September 2019 at SETS, Chennai

Title of Panel discussion: "Need of Indigenous Solutions/products in Securing the Nation's Critical Infrastructure"

Panel Members:



**Dr. Jagadheesh  
Rajaramanna Fellow, BARC, DAE**

S. Jagadeesh, a National Merit Scholar, graduated in Electronics Engineering in First class with Distinction and has graduated in the orientation course in nuclear engineering from the 26th batch of the prestigious BARC Training School. He has worked as Distinguished Scientist & Associate Director E&I (C) Group & Computer Division, BARC. He has made outstanding contributions in the design, development and deployment of Anupam Series Parallel Supercomputers. His outstanding contributions in the areas of system and application software development for parallel processing are note worthy. He is the Chairman of the Infrastructure Group of the ‘National Supercomputing Mission’, Government of India.

He is the team leader from Indian Side for collaborative projects with CERN and has carried out many projects in World Wide Large Hadron Collider Grid (WLCG) in collaboration with IT-Division, CERN.

He has published more than 40 papers in peer-reviewed International and National Conferences. He has taught advanced credit courses in Computer Engineering at post graduate level in ‘Homi Bhabha National Institute’, a deemed university. He is a widely recognized expert both nationally and internationally in the areas High Performance Computing, Grid Computing, Cloud Computing, Artificial Intelligence and Information Security.

Some of the awards bestowed on B.S. Jagadeesh include:

- 1) VASVIK-2017 National Award (for Excellence in Industrial Research) in the category of Information and Communications Technology, by VASVIK Foundation, India
- 2) “Homi Bhabha Science and Technology Award – 2017” from the Department of Atomic Energy, India
- 3) Scientific & Technical Excellence Award - 2007” from the Department of Atomic Energy in the year 2007, India

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Panel Members:



**Dr Sriram Biradavolu**  
**CEO of Telangana Cyber Security COE**  
**Joint Initiative of Telangana Govt. and DSCI**

Dr. Sriram holds PhD in Open Innovation in ICT (Information and Communication Technology) Conferred by Indian Institute of Foreign Trade, in 2016. Also has a Master's in Computer Science Engineering, and a Bachelor's in Electrical Engineering. He has two decades of global experience in the IT/ICT/Telecom industry with an excellent track record of progressive achievements. He has expertise in building high performance organizations that enable and catalyze business & Innovation strategy. Recently he published a book titled “Business Innovation and ICT Strategies” with Palgrave Macmillan, Springer Publications, Europe, in Dec. 2018. He delivered complex, mission critical IT/ICT Enterprise Software for Products, Solutions, Operations and has worked on Consulting engagements globally.

Dr. Sriram is currently the CEO of DSCI's Cyber Security Centre of Excellence, Telangana, Hyderabad. DSCI is a NASSCOM's Initiative. Previously he Headed Information Sciences at T-Hub, India's Largest and Fastest Growing Eco system for Technology Startups, incubating and accelerating about 160 Technology startups in 7 verticals. He has wide leadership experience in reputed technology companies such as ORACLE, VERISIGN, HEWLETT-PACKARD, SIEMENS and TECHSPAN (Goldman Sachs Technology Venture), and have managed large technical and business teams.

Dr. Sriram led large multi-million-dollar global engagements with top and mid-tier customers in the ICT/Telecom/Analytics industry in: USA, Germany, Japan, Singapore, Malaysia, UK, Middle-East, Africa, Philippines and India. He has strong combination of technical, team building, business and innovation capabilities. And passionate about Innovation, AI/ML, Cloud, Analytics, Digital Services, Agile Processes with High Quality. He is a TOGAF 9 Certified Enterprise Architect from Open Group, and a PMP Certified Project Manager from PMI.

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Panel Members:



**Shri. Omveer Singh**  
**Director, Meity**

Mr Omveer Singh is Scientist ‘F’ / Director in Cyber Security R&D Division, Ministry of Electronics & Information Technology (MEITY), Government of India, New Delhi. He had joined erstwhile Electronics Commission (now MEITY), Govt. of India, in September 1983. He was associated with CERT-In from May 2003 to January 2019. At present he is managing the R&D projects on Cyber Forensics funded by the ministry.

At CERT-In, Mr Singh was head of the Cyber Forensics Lab, which was set up by him in 2005 for investigation of cyber security incidents & cybercrimes. He handled around 200 cases of cyber-crime investigations submitted by LEAs to CERT-In during the last 14 years. He was trained on Cyber Security, Advanced Computer Forensics and Advanced Smartphone Forensics at various training institutes of International repute in US, UK and India. He achieved his GCFA (GIAC Certified Forensic Analyst) certification from SANS (System Administration, Networking and Security) Institute in July 2010. He has been conducting training workshops with hands-on-practical sessions and awareness sessions at CERT-In regularly for the LEAs on Cyber Crimes, Computer Forensics, Mobile Devices Forensics, Smartphone Forensics, etc. He is also a visiting faculty at CBI Academy, Ghaziabad; LNJP National Institute of Criminology and Forensic Science (NICFS), Rohini, Delhi; Centre for Development of Advanced Computing (C-DAC), Noida; National Investigation Agency (NIA), Delhi; Internal Security Academy (ISA), CRPF, Mount Abu; Central Police Radio Training Institute, Directorate of Coordination (Police Wireless), MHA, Delhi; Power Grid; Punjab Police Academy, Phillaur; Police Radio Training School, Indore and National Intelligence Academy, IB, Dwarka, Delhi for their training workshops associated with the Cyber Forensic investigations of cybercrimes. He was associated with the judicial academies (Delhi, Odisha, Jharkhand, north-eastern states) and state police for creating awareness through lectures during the workshops on handling of seized digital devices & investigation of cyber-crimes. He was associated in setting up of the cyber forensic investigation labs in the north-eastern states, Jammu & Kashmir, a few other states, SFIO, etc. Earlier, he was also associated with Cyber Security Assurance Programme of CERT-In. Prior to CERT-In, he was associated with C-DAC in implementation of Power SCADA projects. In AAPP, he was associated with development of microprocessor and transputer based systems.

Mr Singh is a graduate in Electronics & Telecommunication Engineering and is having an overall experience of 37 years.



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Panel Members:



**Ms. Smita Purushottam**  
**Founder, SITARA**

Smita Purushottam served as Ambassador to Venezuela (January 2012 to June 2015), Switzerland, Liechtenstein and the Holy See (August 2015-October 2017). In Switzerland, she transformed the sleepy bilateral relationship. After a very long gap, 3 Summits (Prime Minister-President visits) were held in rapid succession due to greater realisation of the benefits of a stronger partnership with the world's No. 1 country in Innovation, international competitiveness, vocational education and environmental performance. Concrete progress was achieved on black money and Swiss support for India's NSG candidature. Several investment commitments from high-tech Swiss firms were received during her tenure.

She also served in India's missions abroad as Deputy Chief of Mission in the Embassy of India in Berlin, Minister (Political) at the High Commission of India, London, the Embassy in Brussels, and Language-trainee and Information Second Secretary at the Embassy of India, Moscow. She made unique contributions in her assignments. Her most formative experience was as Counsellor for Economic and Commercial Affairs at the Indian Embassy in Beijing, where she had the opportunity to see Chinese economic reforms unfold first-hand, which she analysed and relayed back to India. She spent a year as Fellow at Harvard during 2000-2001 where she prepared a paper "Can India Overtake China?" (2001), in which she compared the two economies and their respective reform experiences; analysed the ongoing reforms in the Indian economy focusing on the telecom sector, aviation, privatization attempts and IT, and recommended building a strong manufacturing sector like China had. She emphasised the importance of embedded technology, now called the IOT, and predicted the success of the telecom sector reforms in India. Since then, her focus has switched to China's Science & Technology Advances and her publications have focused on innovation ecosystems inspired by her study of China.

She served also as Joint Secretary at the Foreign Service Institute (MEA), New Delhi; Joint Secretary in the Integrated Defence Staff Headquarters in MOD; Director/Under Secretary (East Europe/Russia/Soviet Union) and SAARC, and Under Secretary (Bhutan) in MEA. She was perhaps one of the few people who predicted that Glasnost and Perestroika would lead to the unwinding of Communist Party rule in the USSR, which was ignored. She predicted the disruptive effects of China's tech acquisitions spree for Europe's technological base and even EU unity, all of which are coming true (the article was rejected as too speculative but that is exactly what happened), and that China's S & T advances contained a powerful challenge for India. As a junior Officer she made other predictions which were ignored as above her pay grade!

She also attended courses at the National Defence University, Washington in July-August 2002, and the Workshop on the Relationship Between National Security and Technology in China at the University of California, San Diego, in July-August 2013. She attended the High-Performance Leadership course at IMD, Lausanne in May 2017. She joined the Institute for Defence Studies & Analyses (IDSA), New Delhi in 2010, as Senior Fellow. Her publications/predictions regarding bridging the increasing gap in S & T with China have been validated by current developments. She also started the High-Tech Defence Innovation Forum to promote high-tech indigenous science and industry, and also the Eurasia cluster to revive studies on this region. She is founder of a new non-profit Do Tank "SITARA", a Science, Indigenous Technology and Advanced Research Accelerator, to bring increased vigour to the indigenisation of high-tech production and building a Science Nation in India. She is a member of the Board of Studies and Adjunct Professor (without remuneration) at the Bharatiya Skills Development University, Jaipur/ Dr Rajendra and Ursula Joshi Foundation, an organisation which she discovered and extended full support to during her stay in Switzerland. BSDU is adapting the world-renowned Swiss vocational education system to Indian conditions.

Ms. Purushottam has been published widely in reputed journals and newspapers in India and abroad.

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Panel Members:



**Dr. N. Sarat Chandra Babu**  
**ED, SETS**

Dr. N Sarat Chandra Babu is presently working as the Executive Director of Society for Electronic Transactions & security (SETS), Chennai. Dr. Sarat obtained his Ph.D. from IIT, Delhi. At SETS he is leading the teams working for Cyber Security specifically in the areas of Cryptology and Computing; Hardware Security; Network Security. He has considerable experience of over three and half decades in R&D, Project implementation and Co-ordination, Education and & training. He is leading the cyber security teams working in Hardware Security, Cryptography (Post quantum & QKD), and network security. Prior to joining SETS, he worked as Executive Director, C-DAC Bangalore and Founder Director of C-DAC Hyderabad. He worked at Department of Electronics, Govt. of India (Presently MeitY) at various levels. He contributed to first digital exchange while working at Indian Telephone Industries Ltd.(ITI), Bangalore. He worked as faculty at REC, Warangal (presently NIT, Warangal).

He is responsible for new R&D initiatives in the areas of Cyber security, Ubiquitous computing, IOT for Smart Cities, system software in NSM etc. He has lead the team in arriving at the Feasibility study report of India Microprocessor. He took initiatives to transfer technology of R&D outcomes to industry. He is passionate in launching new National level workshops/ seminars/ Training programs such as PARCOMPTECH India; WORTICS; Ubicomp India; ELELTECH India/ Hands on with ARM/ Think Parallel etc. He has published around 70 papers in various National and International Journals & Conferences. He is a Senior Member-IEEE, Fellow-IETE, Life Member CSI and Member in ACM.

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Panel Members:



**Shri. Senthil Kumar,  
Associate Director, C-DAC**

R.K.Senthil Kumar is Associate Director in Real Time Systems & Smart Grid Group at Centre for Development of Advanced Computing (C-DAC) Bangalore. He has completed Bachelor's degree in Engineering from Mangalore University, MBA (Systems) Madurai Kamaraj University & PGDCA – CDAC ACTS Amirta Institute of Advanced Computing, Coimbatore, TamilNadu.

He is currently working in the area of SCADA Security and energy trading models for the power sector. He has published over 16 research papers both in national and international conferences. His interests are Smart Grid, Agent based Computing, Power systems and Cloud computing. He is senior member IEEE and life member of Computer Society of India (CSI) and Advanced Computing & Communications Society (ACCS).